



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Tatsuya FUKUNAGA

Group Art Unit: Unknown

Application No.: 10/733,349

Examiner: Unknown

Filed: December 12, 2003

Docket No.: 118003

For: RF MODULE AND METHOD FOR ARRANGING THROUGH HOLES IN RF MODULE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450


Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. The reference(s) was/were cited in a counterpart foreign application. An English language version of the foreign search report is attached for the Examiner's information.
- ☒ 3. English-language Abstracts of the non-English language references are attached hereto.

- ☒ 4. A computer-generated English translation of the following Japanese Patent Publication has been obtained from the website of the Japanese Patent Office (<http://www.jpo.go.jp>), and is attached, but has not been reviewed for accuracy. See Reference 10-082184.

Respectfully submitted,



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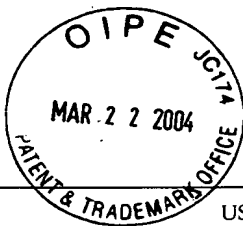
JAO:JSK/kap

Date: March 22, 2004

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Sheet 1 of 1

Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 118003		APPLICATION NO. 10/733,349	
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				APPLICANT Tatsuya FUKUNAGA			
				FILING DATE December 12, 2003		GROUP Unknown	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	
		6,356,172 B1	3/12/2002	Koivisto et al.			
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	
		04-220881 w/Abstract	8/11/1992	Japan			
		10-082184 w/Abstract and Translation	3/31/1998	Japan			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
		Uchimura et al., Hiroshi, DEVELOPMENT OF A "LAMINATED WAVEGUIDE", IEEE Transactions on Microwave Theory and Techniques, Vol. 46, No. 12, December 1998.					
		Ito et al., Masaharu, A 60 GHZ-BAND PLANAR DIELECTRIC WAVEGUIDE FILTER FOR FLIP-CHIP MODULES, IEEE MTT-S Digest 2001.					
EXAMINER					DATE CONSIDERED		
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Date: March 22, 2004